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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Kazuhiro MASUDA

Group Art Unit: 2815

Application No.: 10/799,621

Examiner: M. LANDAU

Filed: March 15, 2004

Docket No.: 119108

For: SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURING THREE-DIMENSIONAL STACKING TYPE SEMICONDUCTOR DEVICE, CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the December 9, 2005 Restriction Requirement, Applicant provisionally elects Group II, claim 2, without traverse.

Respectfully submitted,

James A. Oliff  
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Linda M. Saltiel  
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JAO:LMS/hs

Date: January 9, 2006

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